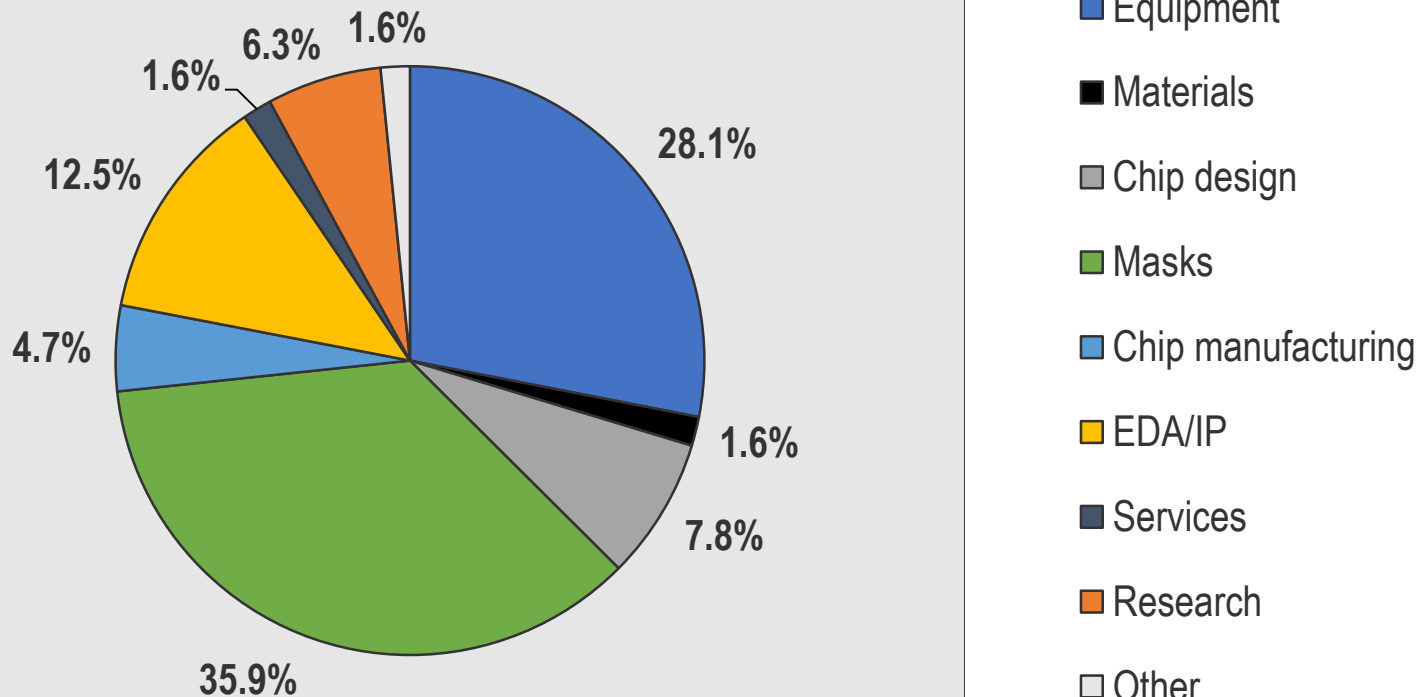


# Your Voice Matters

## eBeam Initiative 2015 Survey



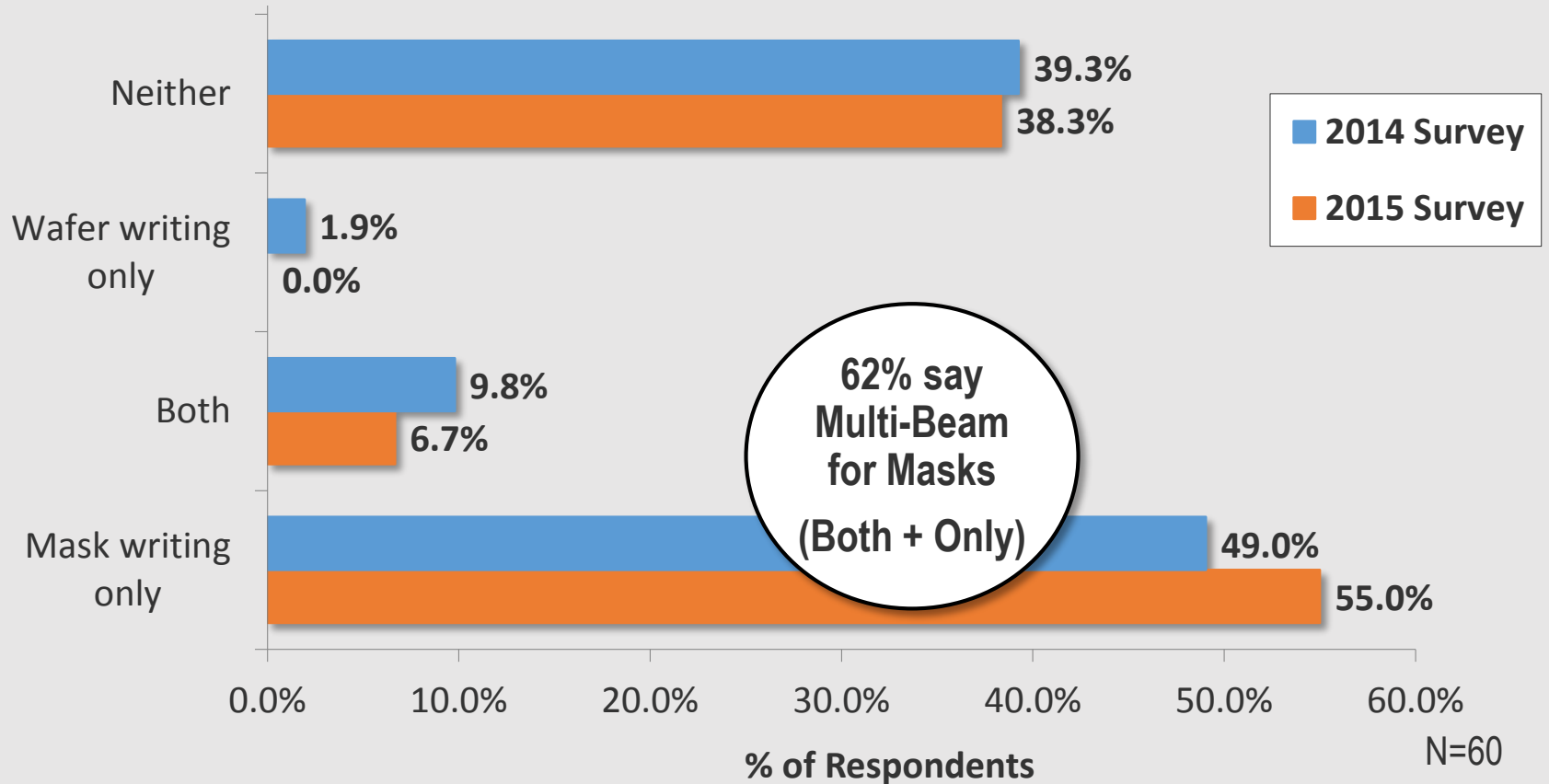
What part of the semiconductor ecosystem is your primary focus?



Thank you to the 64 luminaries and members for your responses  
(35 companies represented)

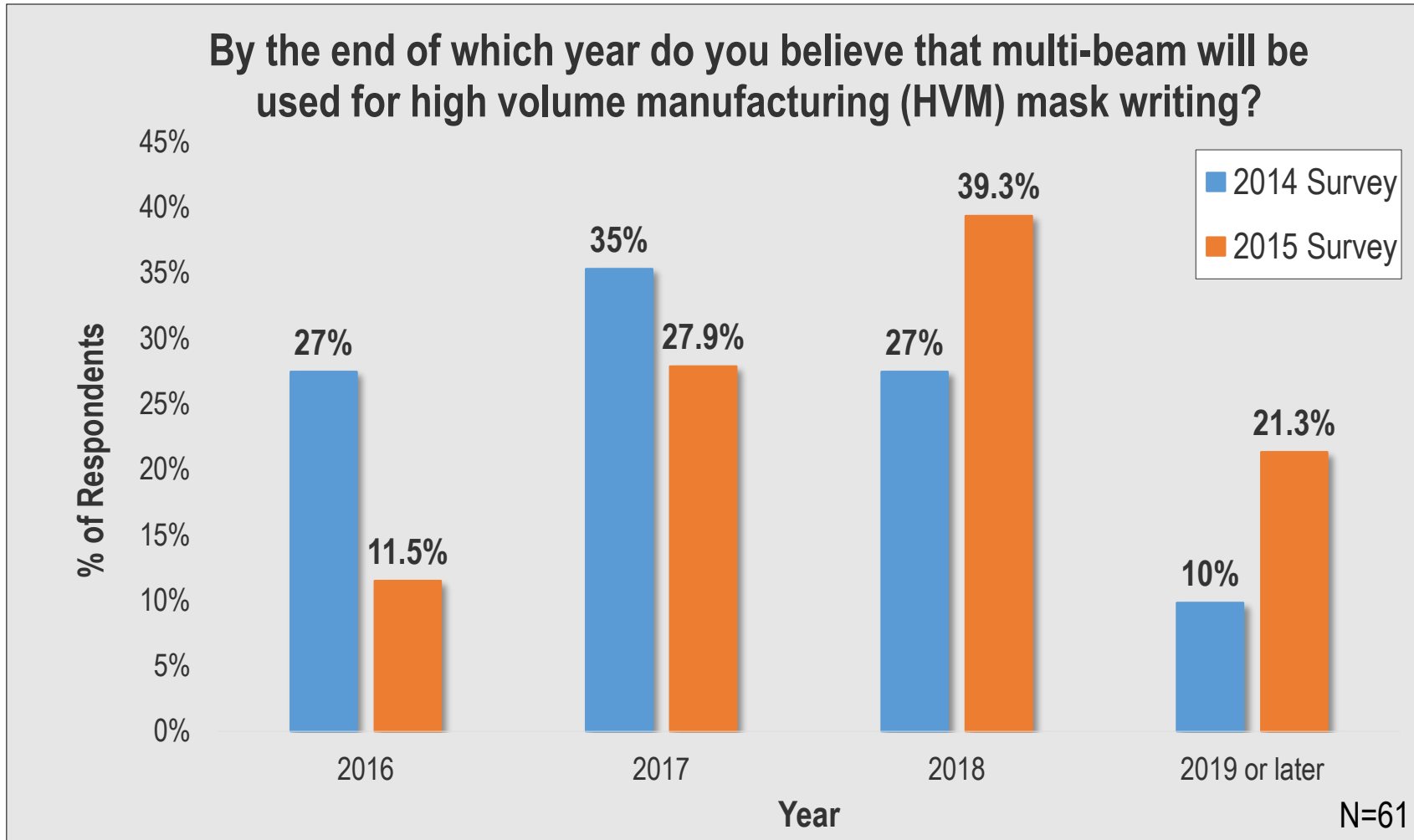
# Vote of Confidence in Multi-beam for Masks

Do you believe that multi-beam technology will be used for mask or wafer writing production by the end of 2016? Select one answer.



# HVM Multi-beam Prediction – Late 2018

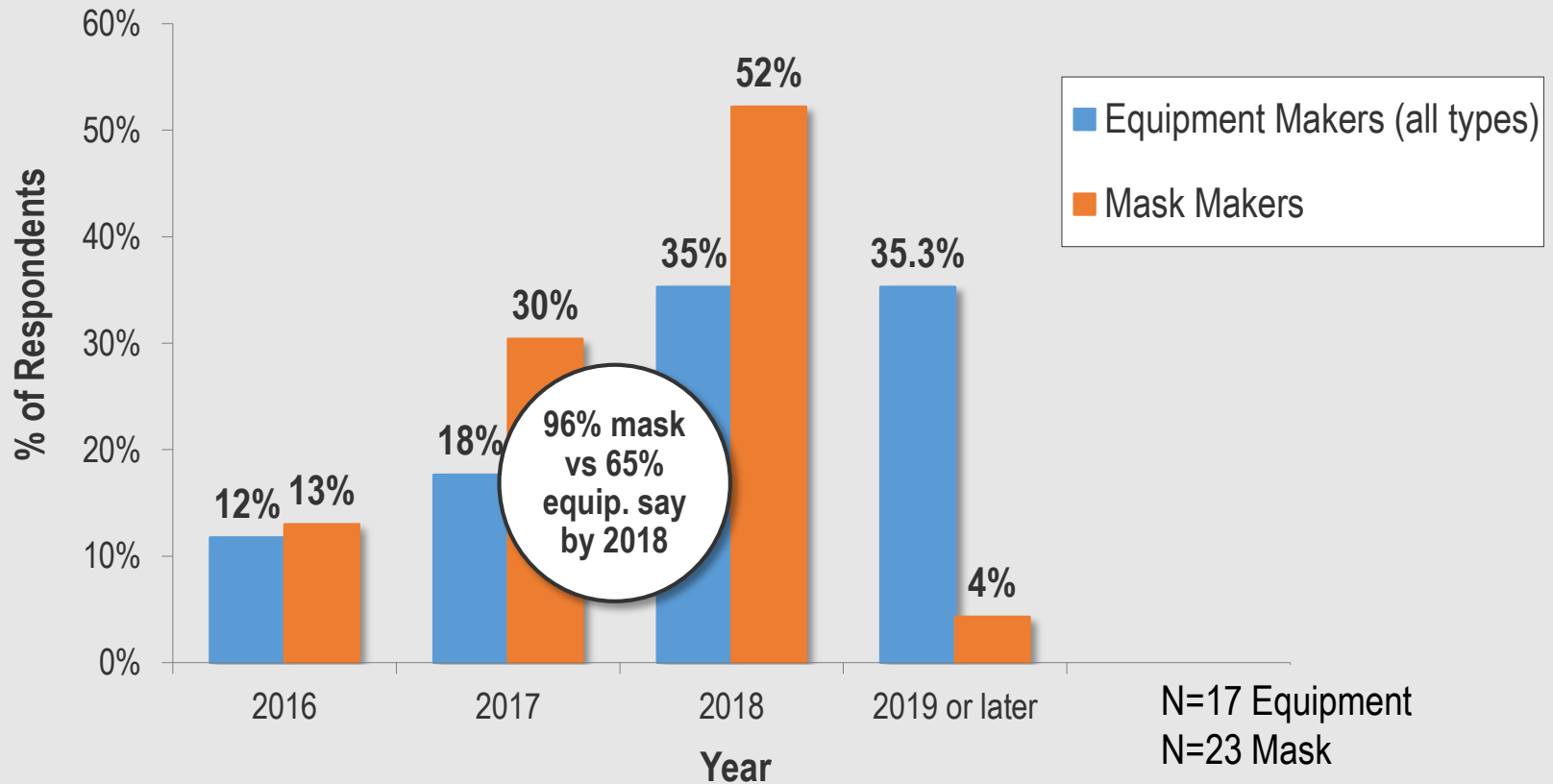
## Weighted average slips 6 months from 2014 Survey



# Mask Makers More Optimistic on 2018 HVM

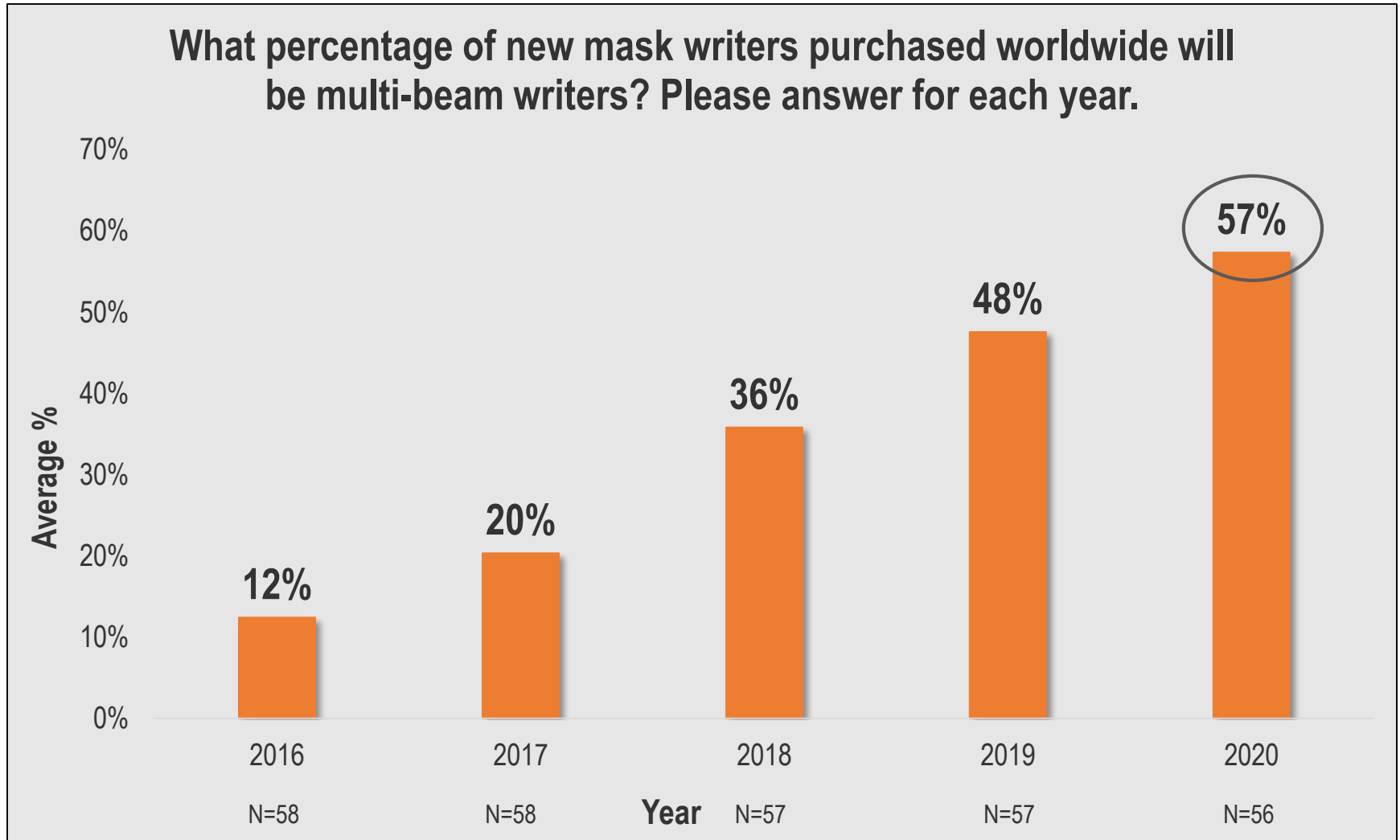
## 96% Mask vs 65% Equipment Makers say by 2018

By the end of which year do you believe that multi-beam technology will be used for high volume manufacturing mask writing? Select one answer.



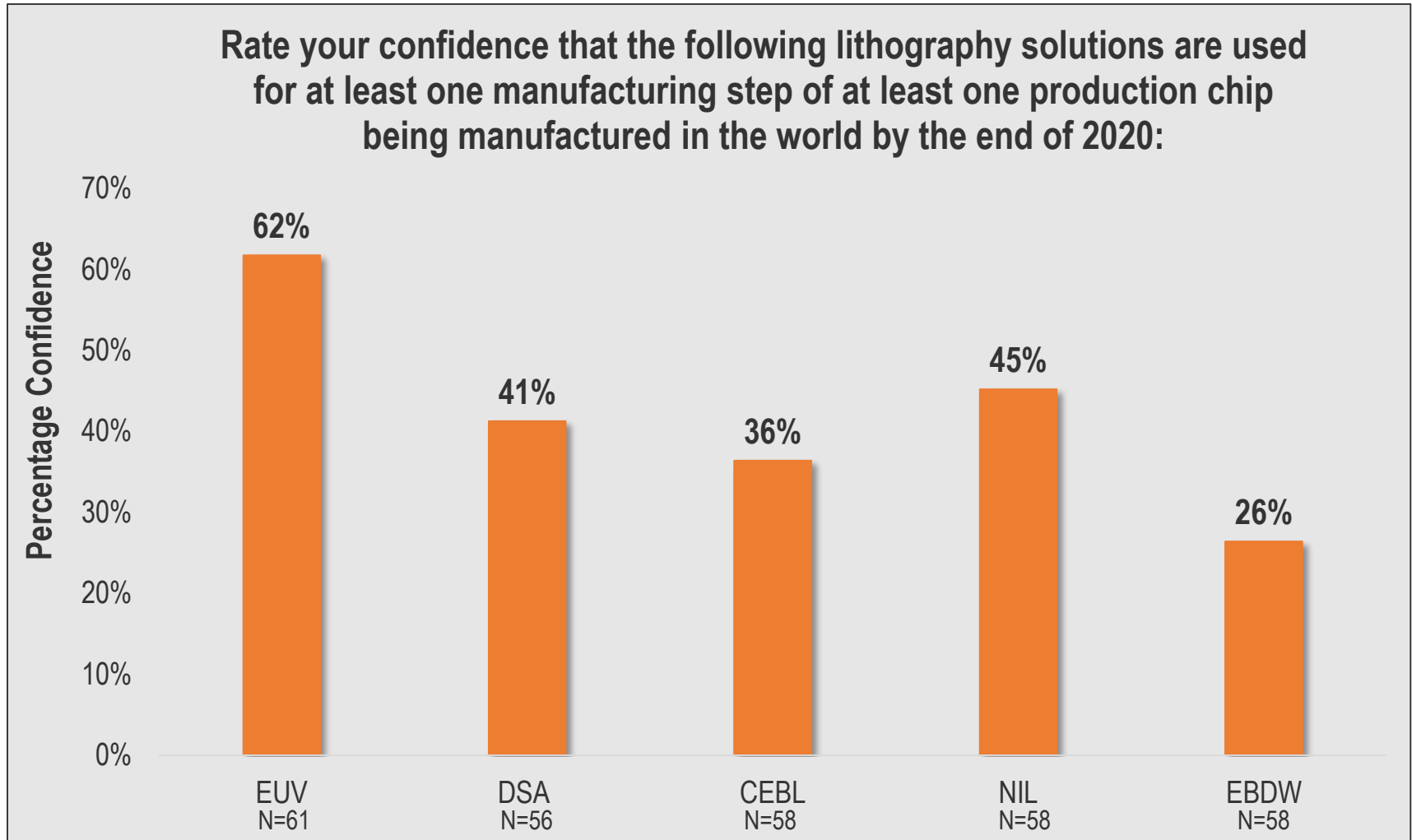
# Purchasing Predictions for Multi-beam

>50% by 2020



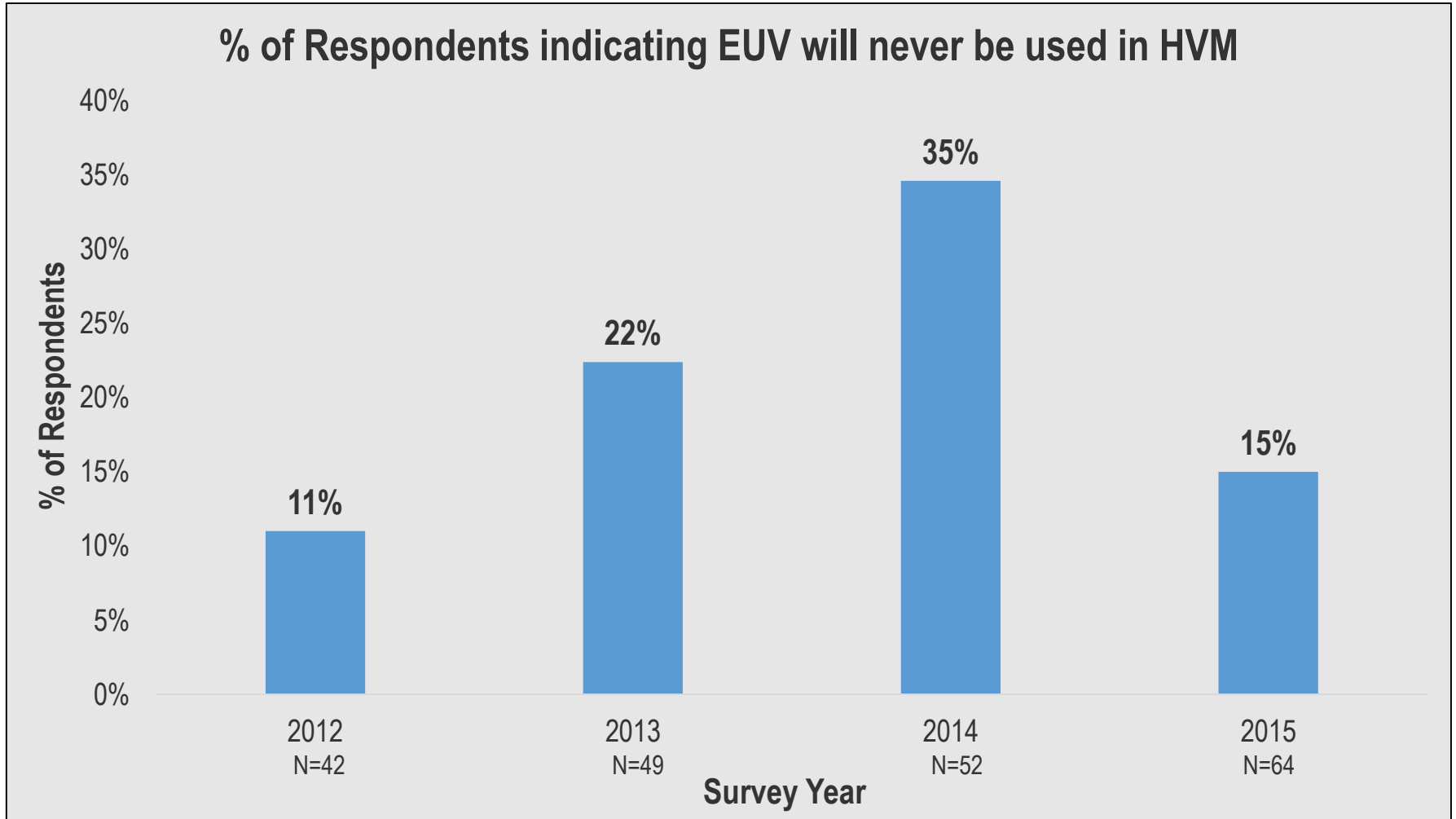
# Lithography Perceptions Favor EUV

## 62% confidence in EUV



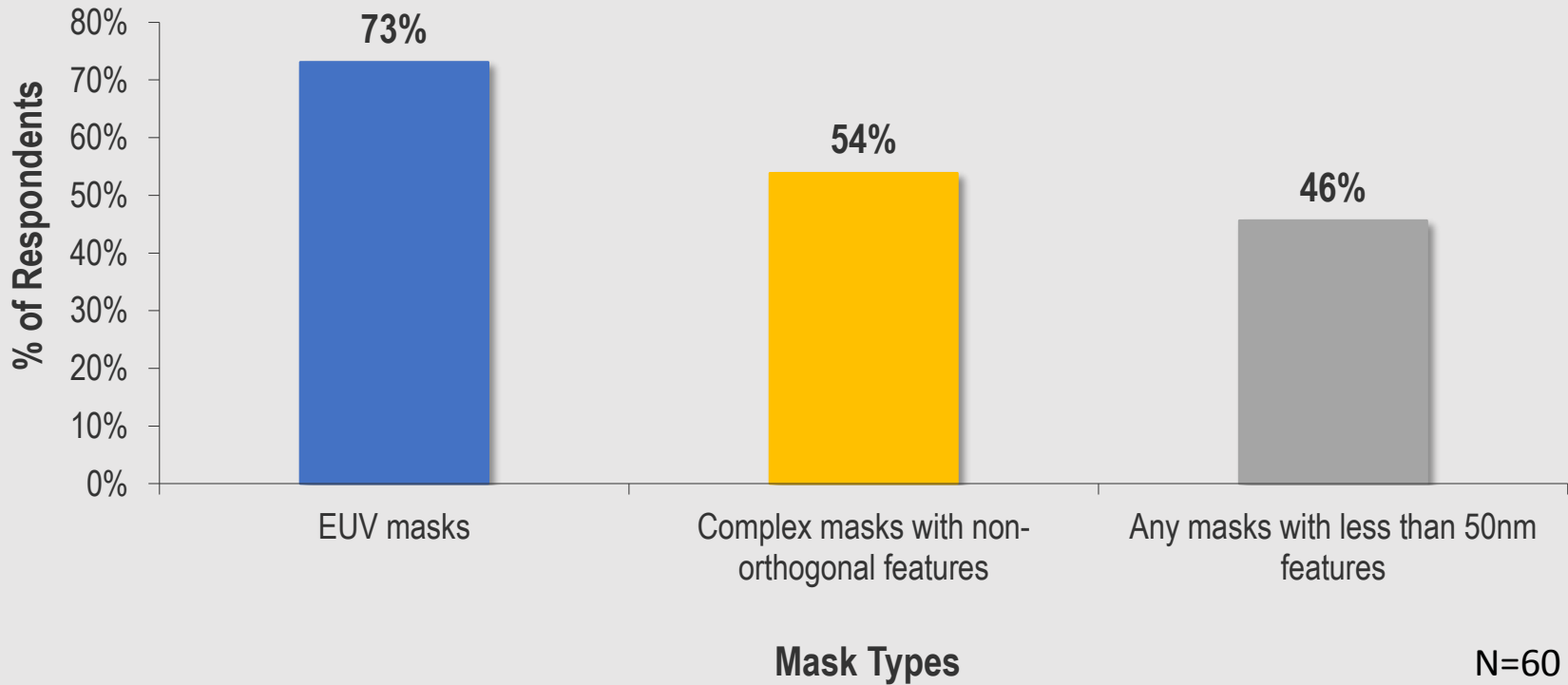
# Optimism in EUV Increased vs 2014

## Respondents answering “Never” down to 15%



# EUV to Drive 3D Mask Effects

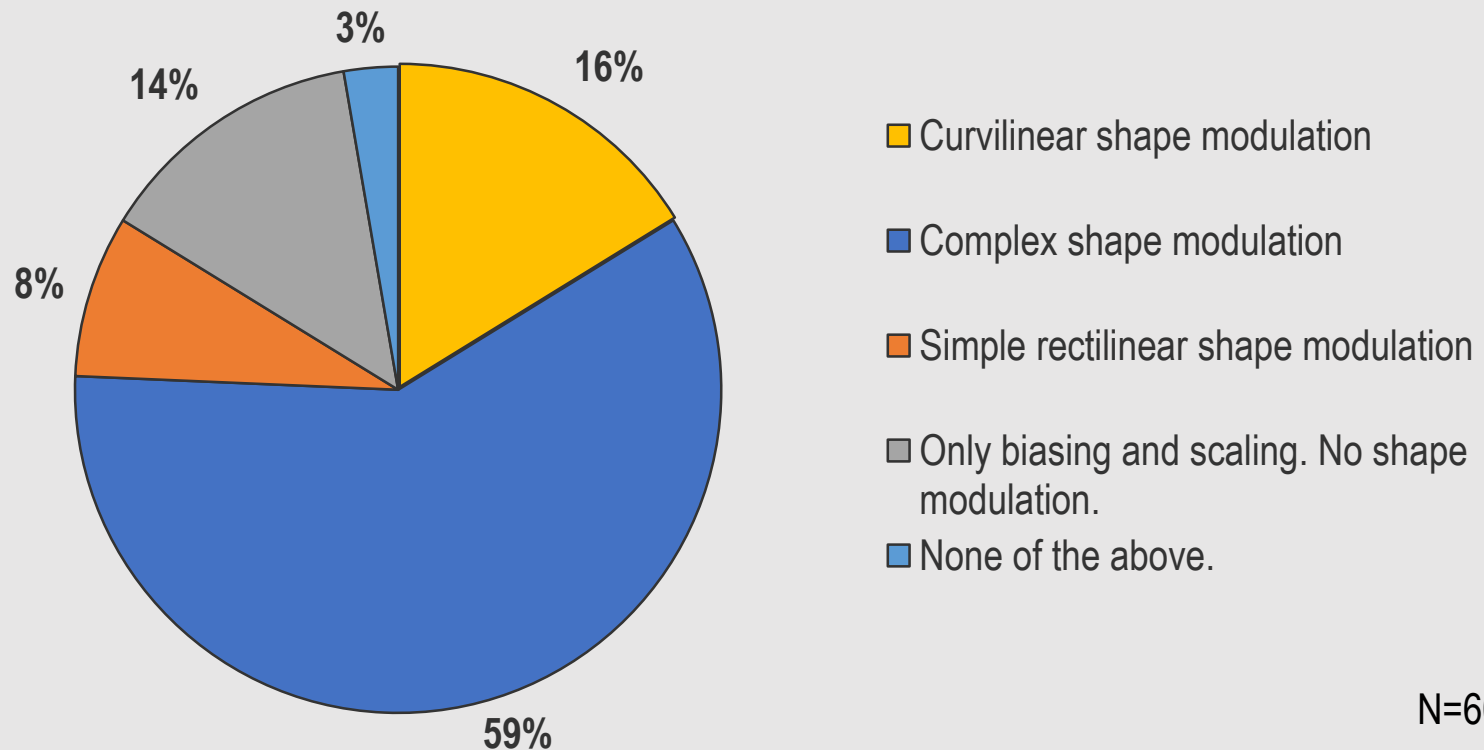
On what type of masks will it be important to model mask 3D effects (including shape-specific sidewall angles)? Select as many as apply.





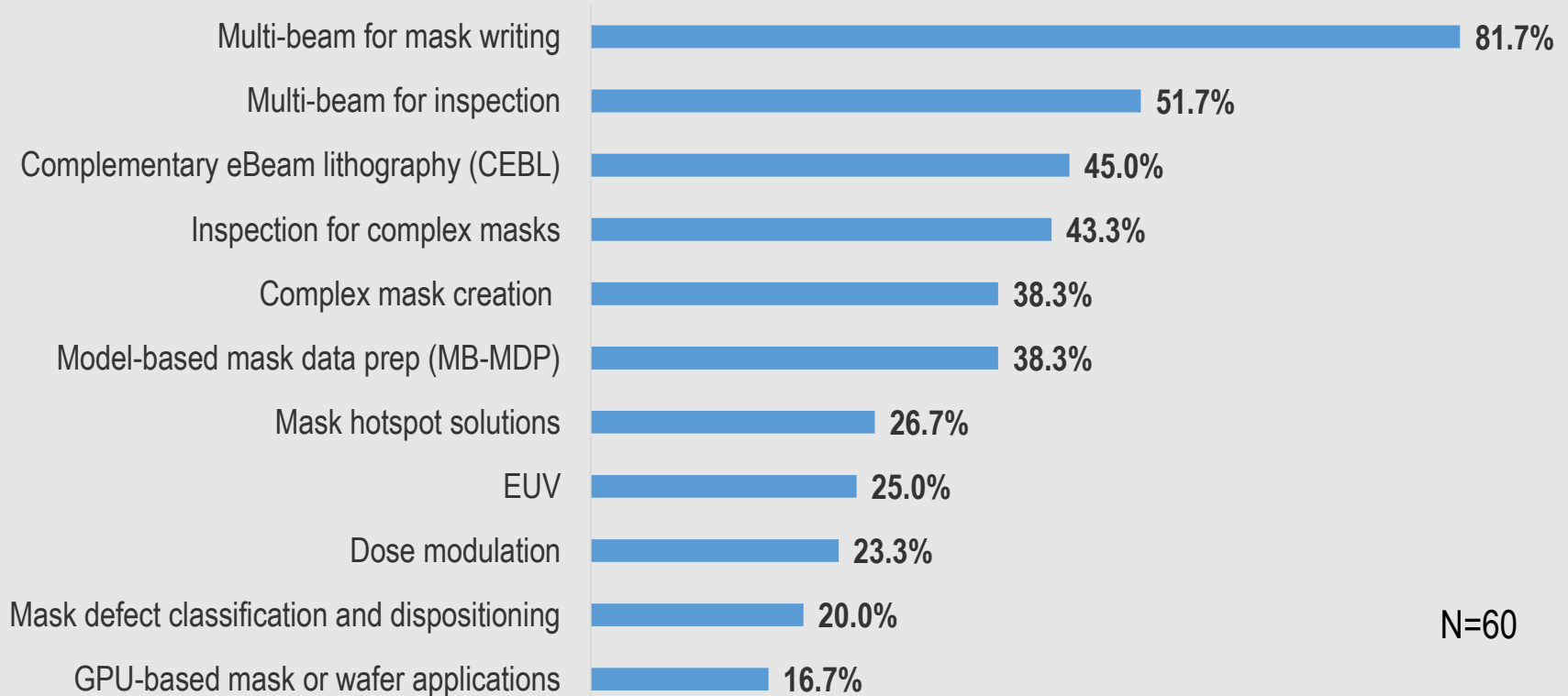
# Complex Mask Shapes Predicted for EUV

How much will CAD shapes on EUV masks differ from CAD shapes for wafer printing due to OPC, shadow/flare correction, or eBeam mid-range scatter correction? Please select one.



# Voicing Your Interests for 2016: Multi-beam for Mask, Inspection and CEBL

**Please select which topics you would like to hear more about from the eBeam Initiative community in 2016. Please select all that apply.**

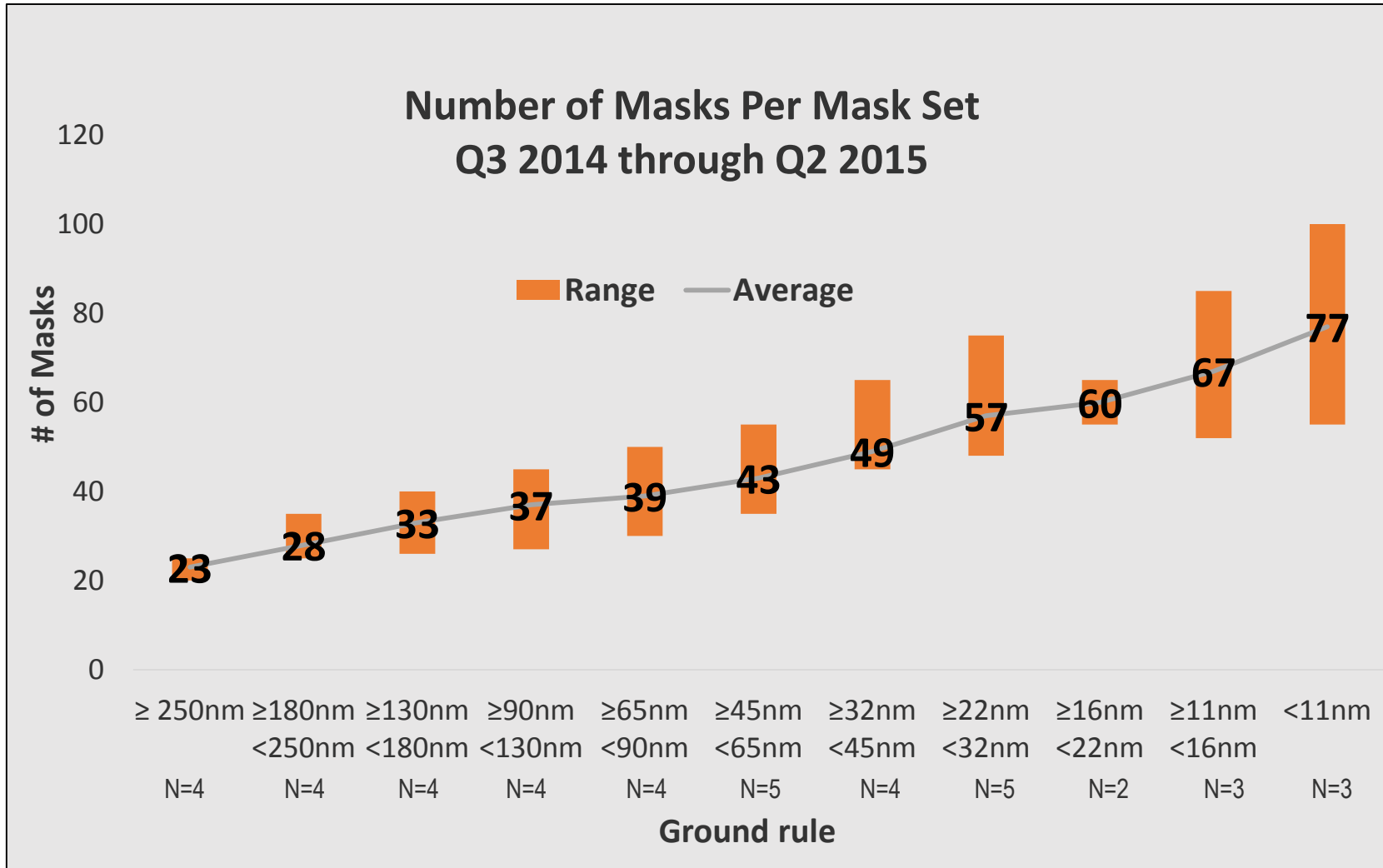


# The Mask Maker Survey 2015

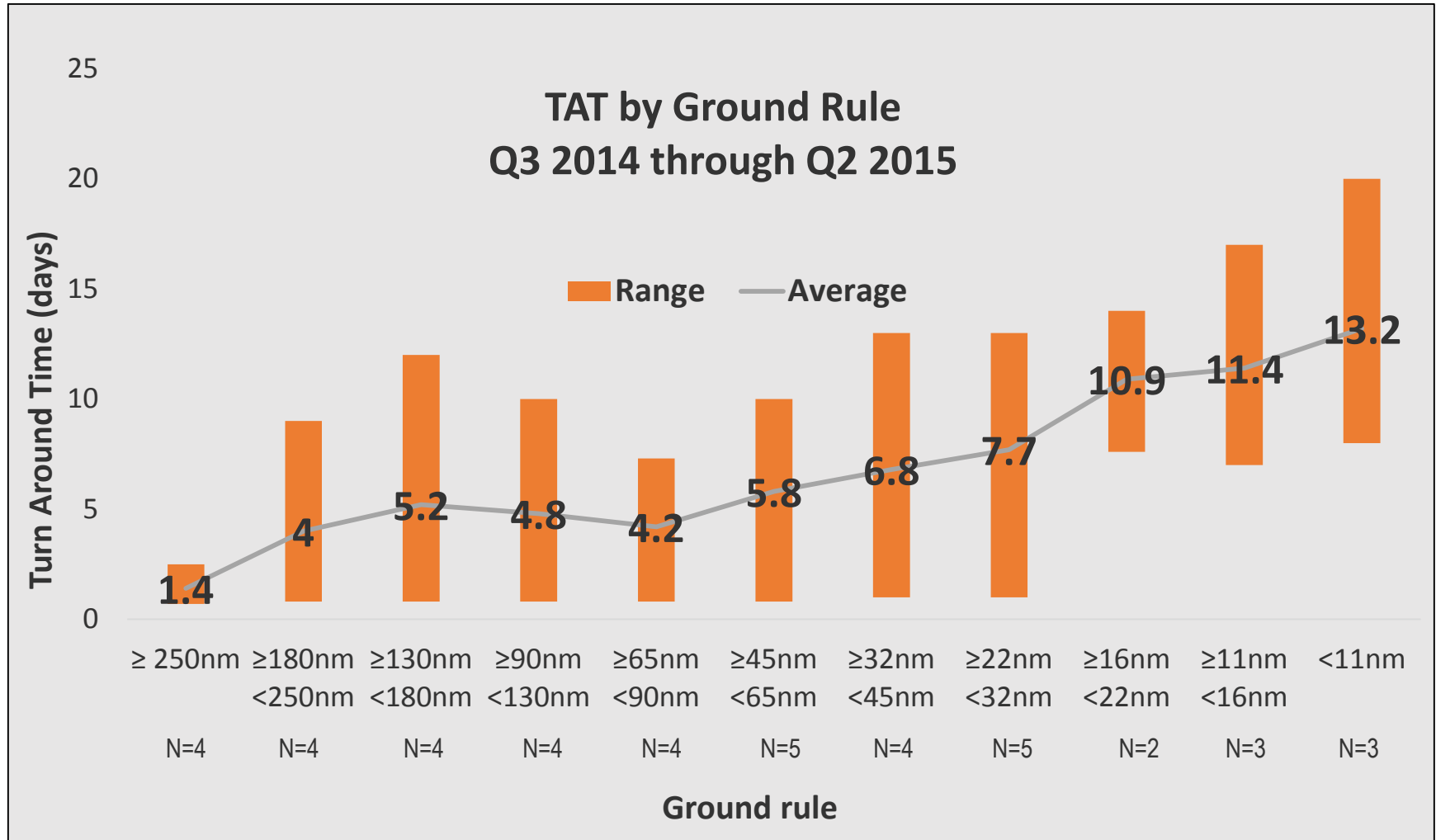
- **Members requested the eBeam Initiative to “fill the gap” which the SEMATECH survey had served through 2013**
  - Thanks to Matt Malloy, SUNY Poly SEMATECH, for his advice
  - This survey did not replicate the past ones
- **Thank you to the 8 participating mask makers:**
  - AMTC, DNP, GLOBALFOUNDRIES (IBM), HOYA, Photronics, Samsung, SMIC, Toppan

# Masks per Mask Set Continue to Grow

Long term 13% per ground rule



# TAT Increases at Smaller Geometries



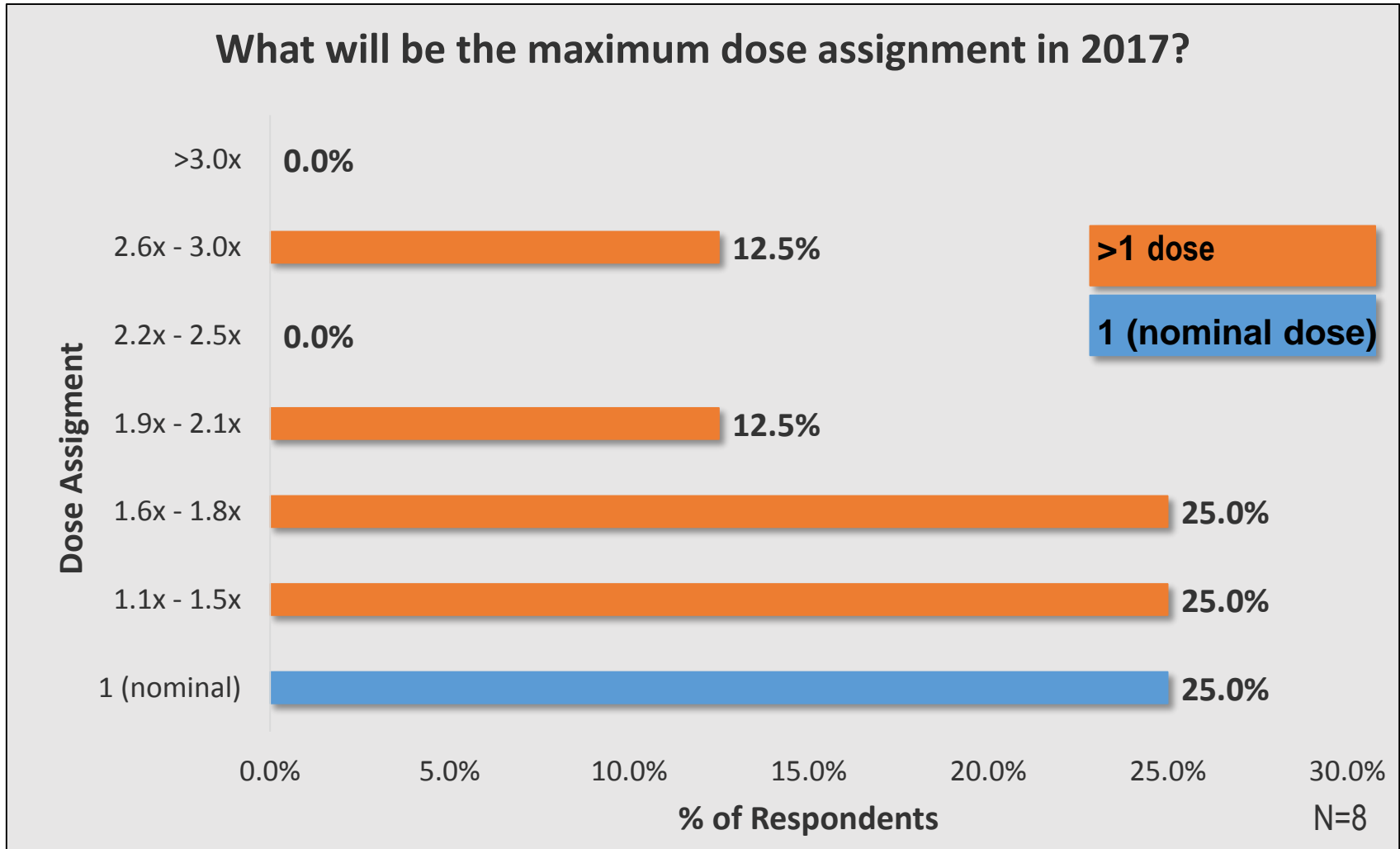
# 2015 Mask Shop Statistics

## Q3 2014 through Q2 2015



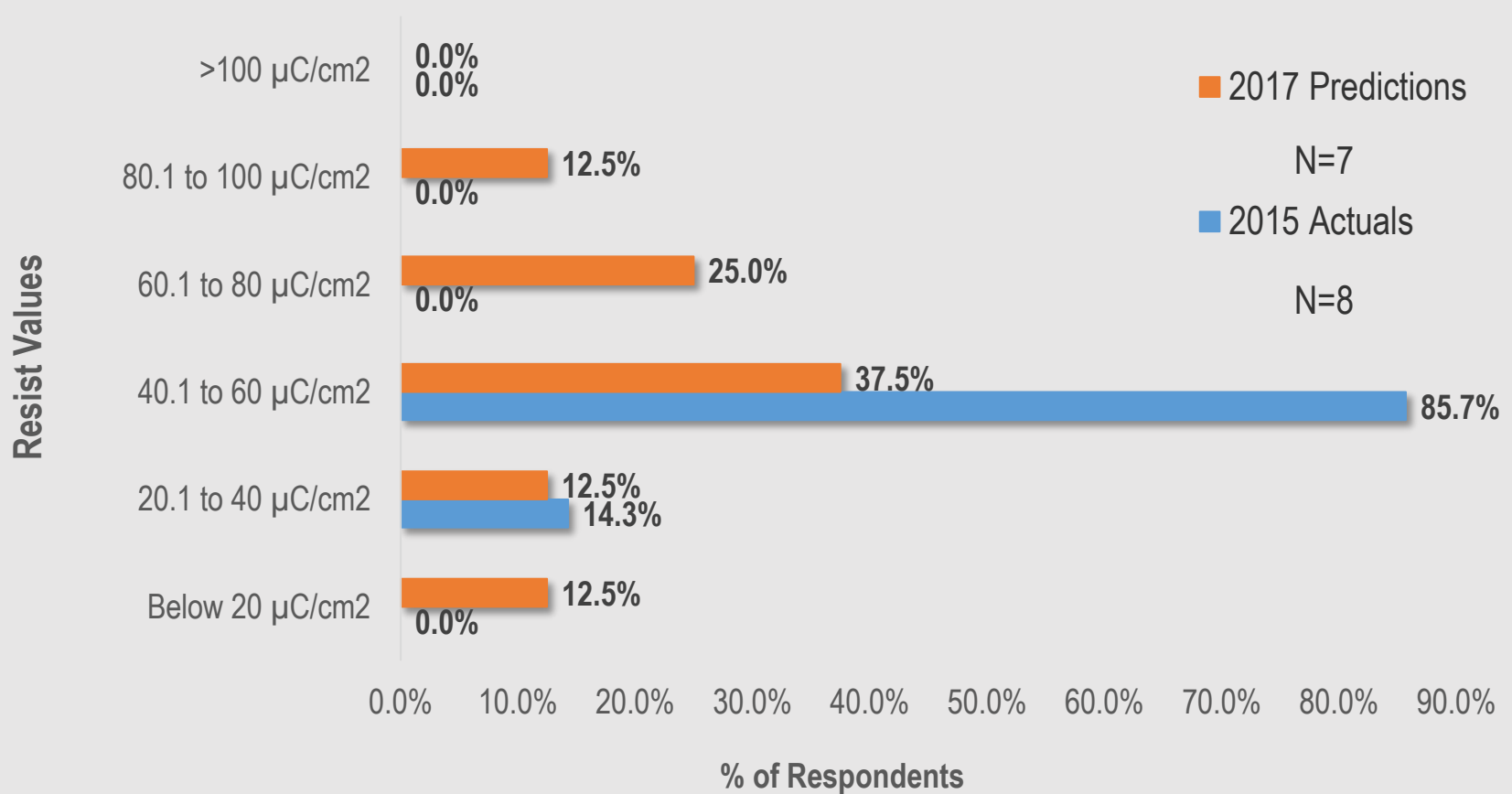
Data	Average	Range	Median
Average mask write time (hours)	9.6	4-16	7
Longest mask write time (hours)	32.7	18-72	29
Average data file size for single mask layer (Gbytes)	38	3-100	20
Largest data file size for single mask layer (Gbytes)	343	55-800	250
Median # of mask defects <0.5 $\mu$ m at 40nm production logic nodes & below	17.7	3-69.7	5.5
% of 40nm & below production masks rewritten	6.8%	1-10%	7%
First repair success rate production masks	86.9%	60-99%	92.5%
Slowest resist used for production ( $\mu$ C/cm <sup>2</sup> )	43.9	20.1-55	40
Max relative dose assigned to shots (1=nominal)	1.5x	1, 1.2x-3x	1.25x

# 75% Plan to Use >1 Dose in 2017



# Range of Resists Predicted to Expand

In 2017, what is the most likely slowest resist to be used for a production mask intended for production wafer manufacturing?





# Complexity Grows for the Mask Maker

- **Longer write times and larger mask sets**
- **Dose modulation is here today**
- **EUV masks will have complex shapes**
- **Multi-beam for mask writing is much anticipated**

**Thank you to those  
who participated in the  
survey!**

**Feedback and questions for future  
surveys welcome – send to  
[jan@williscalibra.com](mailto:jan@williscalibra.com)**