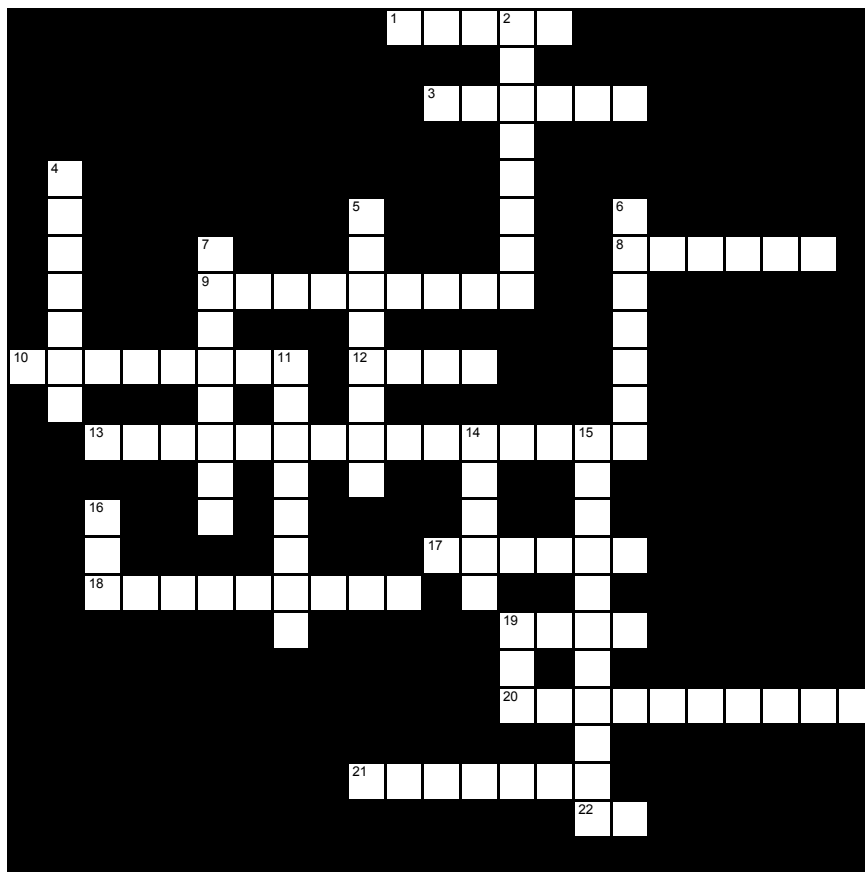


Solve the puzzle and mystery word
 Enter to win a Kindle at www.ebeam.org

Enter by September 30, 2009
 Sponsored by the eBeam Initiative

Across

- 1 EDA member company whose implementation software was used on the first design for e-beam (DFEB) test chip [Hint: see April 2 MUSIC paper at www.ebeam.org]
- 3 Member company based in Germany who supplies e-beam direct write tools with character projection (CP)
- 8 eBeam Initiative member and leading open foundry ASIC provider headquartered in Taipei, Taiwan [Hint: see Members at www.ebeam.org]
- 9 E-beam direct write tool supplier with CP capability based in Japan and used in the first DFEB silicon results [Hint: see May 2009 press release at www.ebeam.org]
- 10 eBeam Initiative steering group member with unique e-beam direct write manufacturing capability and manufactured the first DFEB test chip [Hint: see May 2009 press release at www.ebeam.org]
- 12 eBeam Initiative member company offering lithography optimized design solutions of on-grid, straight line one dimensional layout structures. [Hint: see Members section of www.ebeam.org]
- 13 Forum of more than 20 companies who are educating the market on design for e-beam (DFEB)
- 17 Mask maker and member of the eBeam Initiative motivated by the opportunity to increase the number of designs and who supplies stencil masks for the e-beam direct write tools [Hint: see News & Events/Members Quote Sheet at www.ebeam.org]
- 18 Key metric used in the design flow which directly correlates to e-beam direct write throughput [Hint: see About Us/Technology section at www.ebeam.org]
- 19 Mystery word to enter drawing for a Kindle - Acronym for new technology that enables faster throughput for E-beam direct write machines [Go to www.ebeam.org to enter to win a Kindle]
- 20 A non-production usage of design for e-beam (DFEB) which allows engineers to experiment with innovative



- 21 ideas or to get to a tradeshow on time
- 22 A leading edge European laboratory based in France and driving development in direct write lithography for IC manufacturing [Hint: see Members section of www.ebeam.org]
- 2 Necessary e-beam direct write technology which enables design for e-beam (DFEB) to achieve acceptable throughput [Hint: see DFEB background or About Us/Technology at www.ebeam.org]
- 2 Key barrier design for e-beam (DFEB) direct write is addressing
- 4 eBeam Initiative steering group member based in Japan whose ASIC library and design team collaborated on the first DFEB silicon results. [Hint: see May 2009 press release at www.ebeam.org]
- 5 Chris Anderson's book where half of a market's potential comes from a greater variety of products in smaller quantities [Hint: google Chris Anderson]
- 6 EDA member company whose product, RTL Compiler, has the capability today to optimize shot count early in the design flow [Hint: see Members section of www.ebeam.org]
- 7 Member company providing services and involved in first design for e-beam (DFEB) test chip [Hint: see April 2 MUSIC presentation at www.ebeam.org]
- 11 eBeam Initiative member whose CEO, Jack Harding, is an advisor [Hint: go to www.ebeam.org]
- 14 EDA member company whose product helps characterize design for e-beam (DFEB) library overlays
- 15 This eBeam Initiative member company is a leading provider of semiconductor IP including embedded SRAMs, embedded NVMs, embedded memory test and repair, logic libraries, memory development software, and interface IP solutions [Hint: see Members at www.ebeam.org]
- 16 Company invented design for e-beam (DFEB) and is managing sponsor of the eBeam Initiative [Hint: go to Members section of www.ebeam.org]
- 19 Mask maker and member of the eBeam Initiative motivated by the opportunity to find the optimal lithography solution for low volume production designs [Hint: see News & Events/Member Quote sheet at www.ebeam.org]

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